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12-5-01

501.39983X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Toshihide NABATAME ET AL.
Serial No.: 09/806,861
Filed: April 5, 2001
For: SEMICONDUCTOR DEVICE AND MANUFACTURING
METHOD THEREOF
Group Art Unit: 2812

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

November 29, 2001

Sir:

The following preliminary amendments and remarks are respectfully
submitted in connection with the above-identified application.

IN THE SPECIFICATION:

Please replace the original specification with the attached Substitute
Specification.

IN THE CLAIMS:

Claim 10 has been rewritten as indicated below:

10. (Amended) A method of manufacturing a semiconductor device of
laminating to form a bottom electrode, a dielectric and a top electrode on
substrate having a three-dimensional structure in which,

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